

Mask Set Errata for Mask 1N53H

Introduction

This report applies to mask 1N53H for these products:

- S08PT60
- S08PA60
- S08PT32
- S08PA32

| Errata ID | Errata Title |
|-----------|--|
| 6657 | ADC: ADC FIFO does not work if the bus clock is slower than ADC clock divided by 2 |
| 5264 | DBG: Comparator C with TAG type can not generate breakpoint when setting breakpoint at the address other than instruction opcode address |

e6657: ADC: ADC FIFO does not work if the bus clock is slower than ADC clock divided by 2

Errata type: Errata

Description: When the ADC FIFO mode is enabled, the FIFO can not get correct result if the bus clock is slower than ADC conversion clock divided by 2.

Workaround: Configure the bus clock to be faster than the ADC conversion clock (ADCK) divided by 2 if the ADC FIFO is used.

e5264: DBG: Comparator C with TAG type can not generate breakpoint when setting breakpoint at the address other than instruction opcode address

Errata type: Errata

Description: When setting breakpoint at the address other than instruction opcode address, the comparator C with TAG type cannot generate breakpoint. This issue does not affect code execution.

Workaround: If such tag breakpoint, at the address other than instruction opcode address, is required, use comparator A and/or B tag breakpoint.

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